AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on line 1, page 1 as follows:

The present application is related to pending, commonly assigned, ~~and concurrently filed~~ U.S. Patent Application Serial No. 09/680,545, ~~[Attorney Docket No. 10002222-1 (47607-P226US-10013943)]~~ filed June 3, 2004, entitled "PERSISTENT EMULATED DATA STORAGE USING EXTERNAL STORAGE IN TARGET MODE DISK EMULATOR" which is incorporated herein by reference.

Please amend the paragraph beginning on line 1, page 9 as follows:

FIGURE 2 depicts an apparatus 200 for connecting a host device to an emulator according to a preferred embodiment of the present invention. The embodiment of FIGURE 2 preferably operates to enable more accurate emulation of various peripheral devices emulated by emulator 202 than was possible employing the systems of the prior art. This more accurate emulation is preferably made possible by more accurately representing the physical interface between the host device 201 and a hypothetical peripheral device by employing a plurality of connection lines between the host device 201 and emulator 202. Although the embodiment of FIGURE 2 depicts three such connection lines, it will be appreciated that fewer or more than three such connection lines may be employed, and that all such variations are included within the scope of the present invention. Although the following discussion generally concerns an embodiment in which host device 201 is connected to a single emulator 202, where emulator 202 emulates a single device, it will be appreciated that multiple emulators may be connected to host device 201, and multiple peripheral devices could be emulated by any single emulator, and all such variations are included in the scope of the present invention.

Please amend the paragraph beginning on line 21, page 13 as follows:

FIGURE 4 depicts an apparatus 400 for use in interfacing an emulator to a host device according to a preferred embodiment of the present invention. FIGURE 4 includes some of the same features depicted in FIGURE 2, such as host device 201, emulator 202, and the data, control, and power data lines. However, FIGURE 4 depicts certain preferred interfacing

components for implementing the inventive set of connections between host device 201 and emulator (or, target mode emulator) 202. In a preferred embodiment, host device 201 may be implemented as part of a high availability storage system. Alternatively, host device 201 may be associated with a number of other data processing applications.

Please amend the paragraph beginning on line 16, page 15 as follows:

FIGURE 5 depicts a control interface connection 500 between host device 201 and emulator 202 according to a preferred embodiment of the present invention. FIGURE 5 depicts one possible selection of control interface connections which may be implemented within control data line 204 and power data line 205 (FIGURE 2). It will be appreciated that fewer or more connections may be established, and that connections other than those shown in FIGURE 5 may be implemented within control data line 204, and all such variations are included within the scope of the present invention.

Please amend the paragraph beginning on line 27, page 16 as follows:

FIGURE 6 depicts an example of logic flow 600 during a device emulation according to a preferred embodiment of the present invention. The logic flow 600 depicted in FIGURE 6 generally assumes that the device being emulated has a motor or other analogous active electro-mechanical device. Reference may be made to FIGURE 2 in connection with the mention of host device 201 and emulator 202.

Please amend the paragraph beginning on line 8, page 17 as follows:

At step ~~201~~ 602, host device 201 preferably determines whether a data interface on emulator 202 is enabled by checking DIE lines 502 (FIGURE 5). If the data interface is not enabled, generally no response will be provided by host 201, as indicated in step 606. If the data interface is enabled, host device 201 preferably determines whether emulator 202 is available and connected by checking power supply detection line 505, step 603. If emulator 202 is not available, a response will generally not be provided by host device 201, as indicated in step 606. If emulator 202 is available, the host device preferably proceeds to determine the identity of the device being emulated.

Please amend the paragraph beginning on line 22, page 17 as follows:

If the correct device is being addressed by host device 201, the inventive mechanism then determines whether host device 201 seeks user data or product identification information, step 605. (Herein, "user data" generally refers to data stored on a hard disk drive or other storage device as opposed to overhead data which describes the identity and location of the disk drive or other emulated device). If the requested information in product information, which generally does not require activating a disk drive motor or otherwise delving into user data storage within an emulated storage device, then the product information is returned to host device 201 at step 608. If the requested information is user data, the inventive mechanism then determines the status of the disk drive or other storage device motor at step 607. In an emulation environment, the status of the "motor" or storage device active mechanism is generally a bit or flag established by software operating within emulator 202 as opposed to a condition of an actual physical device.